

Session Traitements

NEUROMORPHIC CIRCUITS

Lorena ANGHEL

TIMA

WORKSHOP

L'Intelligence Naturelle au coeur
des enjeux de l'Intelligence Artificielle

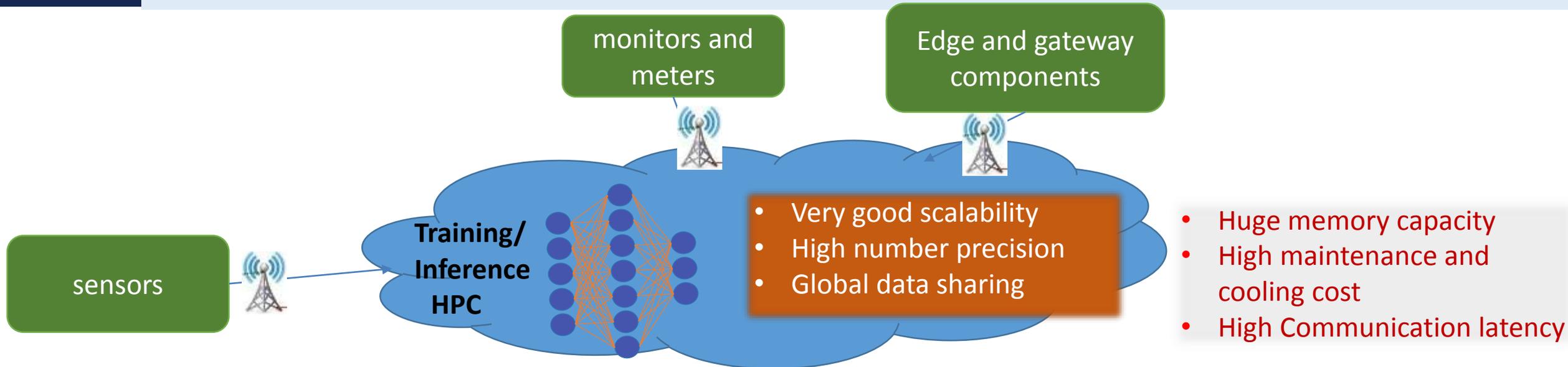
NeuroCoG
Unité Grenoble Alpes

INSIS
GRENOBLE
Cognition

tima
CNRS - Grenoble INP - UFR

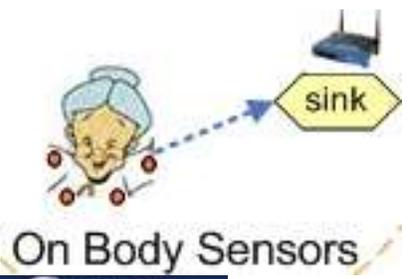


From Cloud DNN to Embedded Hardware DNN



Deep Intelligent Solutions

data collection, predictions, diagnosis, classification, anomaly detection, correlations, pattern detection, decision making



On Body Sensors



e-health



smart cities



Manufacturing

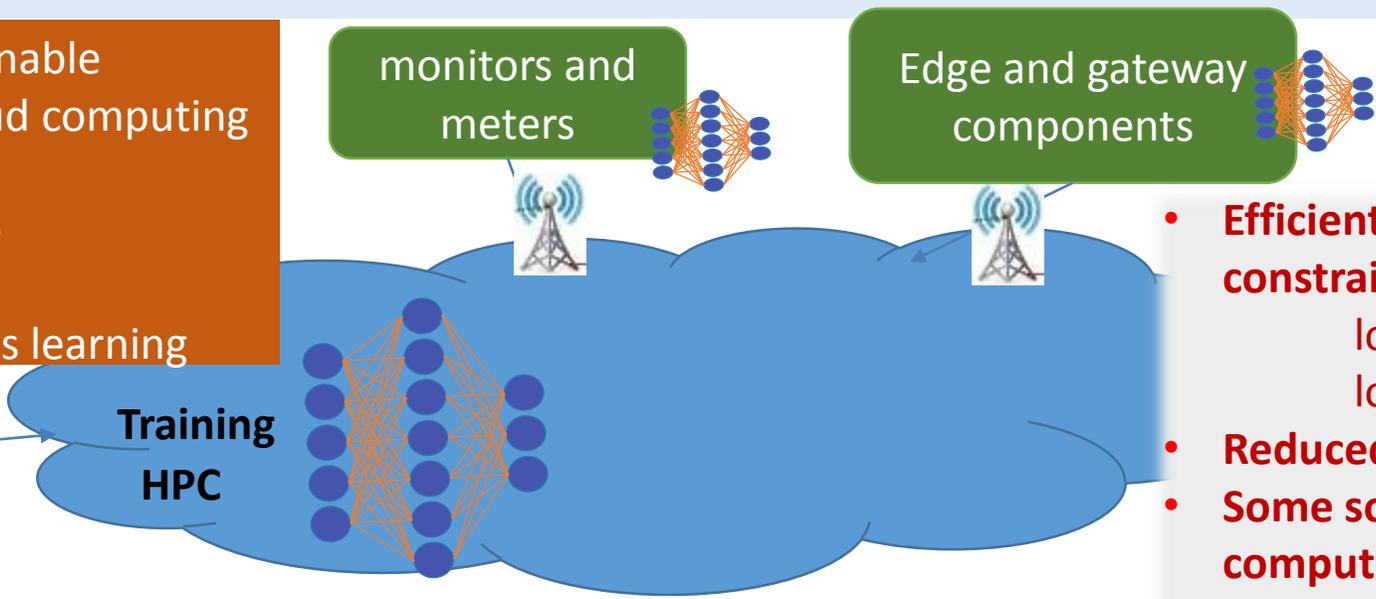
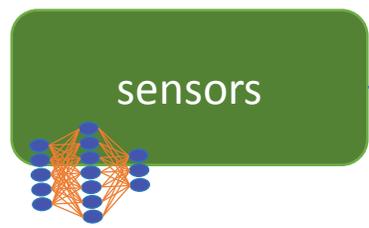


autonomous vehicles



From Cloud DNN to Embedded Hardware DNN

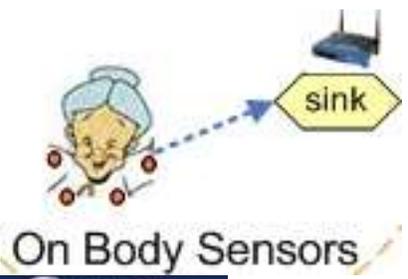
- High Latency and questionable Security related to the cloud computing are intolerable
 - On device intelligence
 - Real-time operation
 - Real-time autonomous learning



- **Efficient deployment on HW constrained resources**
 - low power operation
 - low memory capacity
- **Reduced number precision**
- **Some solutions need to change computing paradigm**
 - non Von-Neuman computation

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e-health



smart cities



Manufacturing

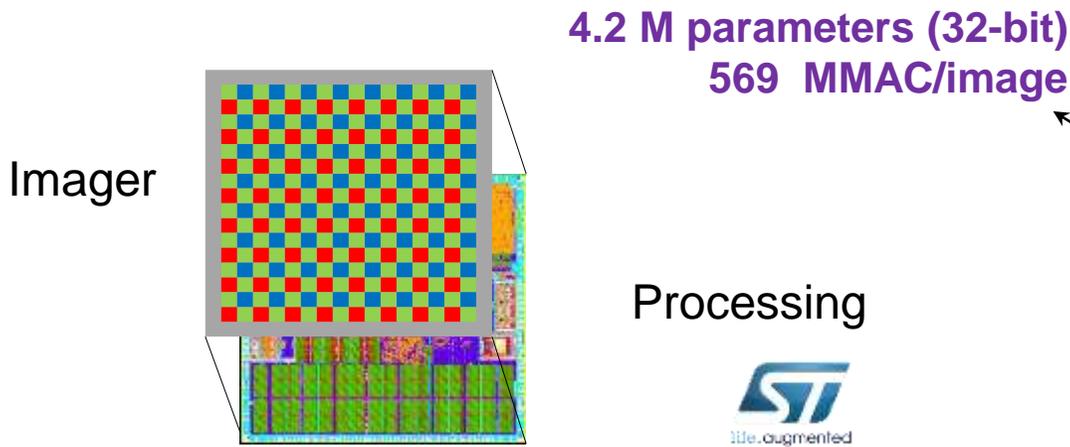


autonomous vehicles



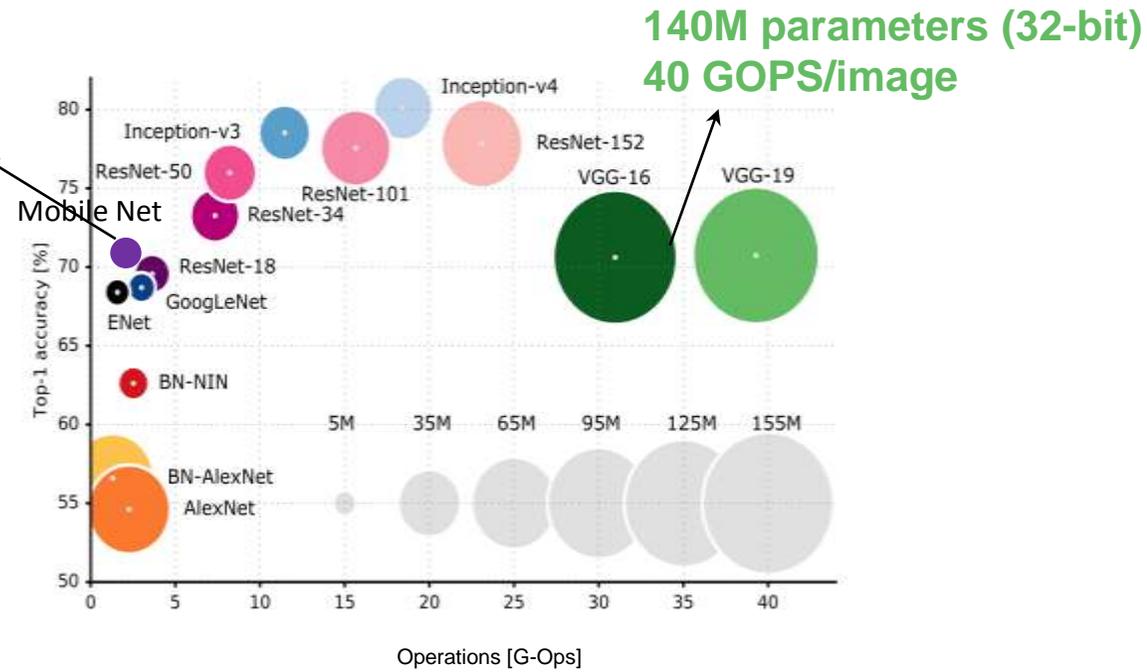
HW Solutions for Embedded Vision AI

How to design « smart » imagers, mixing acquisition and processing in a single device ?



The Imager and Processing dies are 'stacked' to form a single device. The computing resources are limited :

- 1.5 MB to 12 MB memory
- Power << 100 mW



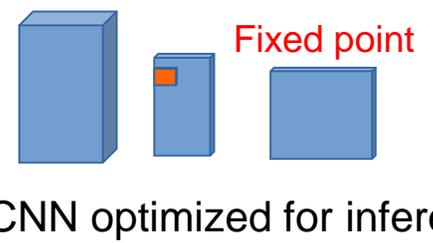
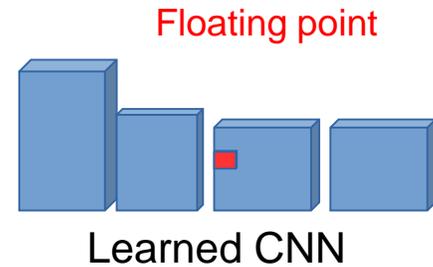
CNNs requires huge computing power and memory footprint (coefficients and temporary data)



HW Solutions for Embedded Vision AI

How to design « smart » imagers, mixing acquisition and processing in a single device ?

- Optimize for non-functional metrics : memory, computing power, power consumption
 - **impact on CNN architecture and learning**
- **CNN compression/decompression**
 - Save computing power
 - Save memory throughput and footprint
- Efficient **hardware** (specialized operators), and embedded **software** (customized processors) for CNN inference
 - Exploit all levels of **parallelism** and reduce overheads
 - Dedicated internal hardware support (decompression)

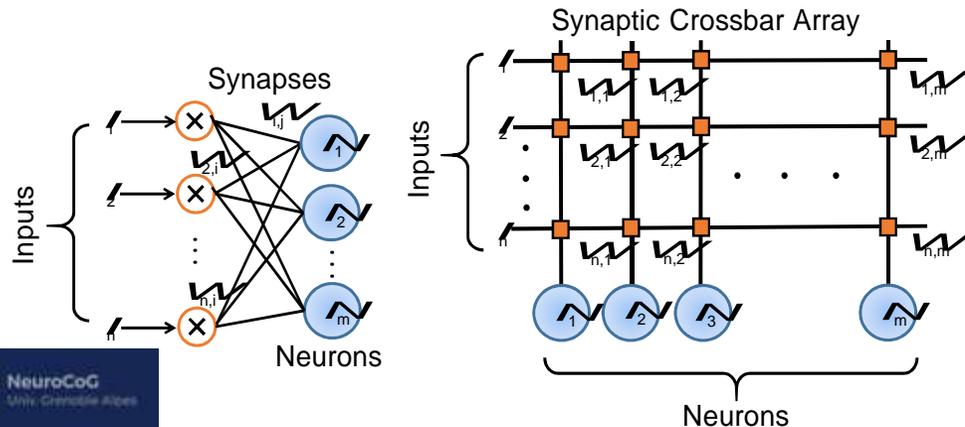
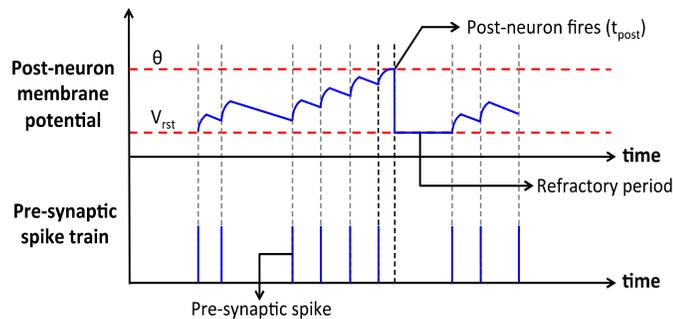
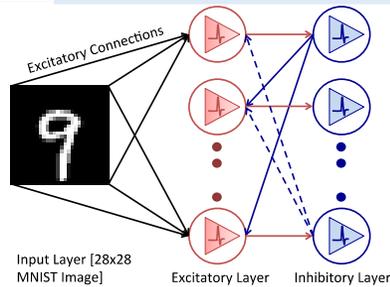


Compressed CNN

Data and coefficients transforms



Brain-Inspired HW Neuromorphic Solutions



Spiking Neural Networks

- Mimic brain-like computation – use data encoded as spikes
- Bio-inspired learning rule – un-supervised Spike Timing Dependent Plasticity
- Higher energy efficiency – event driven computation (20x less - fJ)

Applications

- Sparse data and multi modal information processing, belief, probabilistic and prediction engines, pattern recognition
- IOT, mission critical systems, high security data, personalized medicine

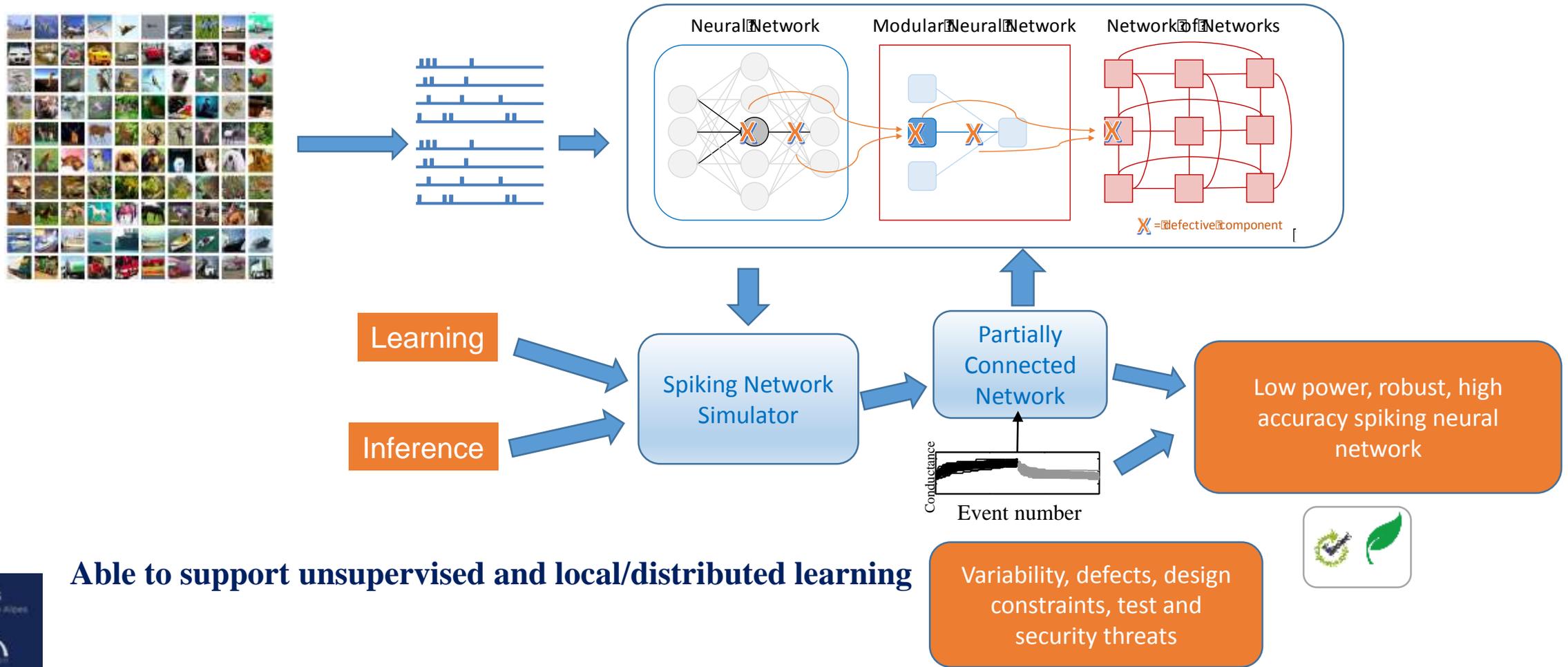
Approaches

- Kernel Based computation – one kernel per NN layer
- Use hybrid CMOS-emerging devices (Magnetic/Resistive RAM)
 - getting closer to the stochastic neuron and inference and decision making resembling bayesian inference
- Use Computing-in-Memory Paradigm



Brain-Inspired HW Neuromorphic Solutions

Design of Ensemble Architectures based on massive network pruning defined by learning, defect, reliability and security attack strategies



Able to support unsupervised and local/distributed learning



Interactions and Collaborations

- **Applications**

- ❖ Computer vision
- ❖ Speech and language/text recognition
- ❖ Information management from sensors (wireless sensing in e-health) - prediction
- ❖ Biomimetic methods and applications in medical imaging and computer aided surgery
- ❖ Autonomous vehicles (cars, robotics, etc)

ST Microelectronics
TIMC
LPNC

- **Technology and Architectures**

- ❖ CMOS technology last generations
- ❖ Emerging technologies: magnetic, resistive, nanowire
- ❖ Non von-Neuman computing paradigms

ST Microelectronics,
SPINTEC Lab
CEA

- **New Algorithmic Approaches**

- ❖ Statistical Modelling and Big Data approaches for better energy efficiency

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